

Docket No.: 239636US2

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COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/608,027

Applicants: Hiroyuki KIMURA, et al.

Filing Date: June 30, 2003

For: FLAT PANEL DISPLAY DEVICE HAVING DIGITAL

MEMORY PROVIDED IN EACH PIXEL

Group Art Unit: 2674 Examiner: TRAN, H.

SIR:

Attached hereto for filing are the following papers:

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Our credit card payment form in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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DOCKET NO: 239636US2

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

HIROYUKI KIMURA, ET AL. : EXAMINER: TRAN, H.

SERIAL NO: 10/608,027

FILED: JUNE 30, 2003 : GROUP ART UNIT: 2674

FOR: FLAT PANEL DISPLAY DEVICE HAVING DIGITAL MEMORY PROVIDED

IN EACH PIXEL

<u>COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE</u>

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant acknowledges with appreciation the indication of allowability of the claimed invention. In response to the Examiner's Statement of Reason for Allowance in the Notice of Allowance of December 16, 2005, Applicant respectfully submits the following comments.

In the Examiner's Statement of Reasons for Allowance on page 2 of the Notice of Allowance mailed December 16, 2005, paragraph 3 states in part:

Each independent claims 1 and 3 identifies the uniquely distinct features: "assign(ing) as write frames at least initial two frame following the completion of the switching ... in order to allow still image data to be written in digital memory (18)" and "at least one of the rise times of a memory control signal supplied to a switch circuit (17) and a common signal supplied to the common electrode (15) is made longer than vertical blanking period immediately following the completion of the typical display."

(Emphasis in original.)

However, it is respectfully noted that the drawing labels 15, 17, and 18 included in the

above Reasons do not appear in Claims 1 and 3. These drawing labels refer to the exemplary

embodiments of the present invention shown in Figures 3 and 4 of the present application.

As these drawing labels are not part of the claims, it is respectfully noted that they should not

be used to construe the claims, nor should the claims be in any way limited to the exemplary

embodiments shown in the figures of the present application.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

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